## **REMARKS**

The Office Action in the above-identified application has been carefully considered and this amendment has been presented to place this application in condition for allowance.

Accordingly, reexamination and reconsideration of this application are respectfully requested.

Claims 1, 6, and 11–14 are in the present application. It is submitted that these claims were patentably distinct over the prior art cited by the Examiner, and that these claims were in full compliance with the requirements of 35 U.S.C. § 112. The changes to the claims, as presented herein, are not made for the purpose of patentability within the meaning of 35 U.S.C. sections 101, 102, 103 or 112. Rather, these changes are made simply for clarification and to round out the scope of protection to which Applicants are entitled. Claims 2-5 and 7-10 are canceled. Claims 11-14 are added.

Claims 1–4 and 6–9 were rejected under 35 U.S.C. § 102(b) as being anticipated by Circello et al. (U.S. Patent 5,964,893).

The present claims recite both "bus-access detection means for detecting, in real time, based on internal bus information, each match of an address set with an address line on said internal bus by said controller" and "execution address detection means for detecting execution address information of said external monitoring means in real time based on said internal bus information." (Claim 1; Claim 6 contains similar limitations) As shown in Figure 1, the internal bus-address comparing circuit 6 is separate and distinct from the execution address comparing circuit 7. Moreover, each circuit performs a distinct function; namely a bus-address comparison and an execution address detection.

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The Examiner contends Circello's debug module meets the present invention's "busaccess detection means" at Column 3, lines 24-30 and Column 5, lines 40-49. (Office Action page 3) The Examiner further contends this same debug module meets the present invention's "execution address detection means" while citing the same locations in Circello. Assuming arguendo that Circello's debug module is analogous to the present invention's "bus-access detection means," Applicants are unable (based on the disclosure locations cited by the Examiner) to determine how the same debug module can also meet the "execution address detection means" which is physically distinct and performs a different function. Accordingly, Applicants do not believe the debug module meets the present invention's execution address detection means and respectfully request the Examiner point out specific functions of the debug module that he believes meets both circuits.

Therefore, for at least this reason, Circello fails to anticipate the present invention and the rejected claims should now be allowed.

Claims 5 and 10 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Circello in view of Fasang (U.S. Patent 4,433,413). The limitations of claims 5 and 10 have been amended into claims 1 and 6, respectively. Regardless, Fasang is relied on solely to meet the numerically displayed data limitations recited in the rejected dependent claims. Accordingly, for the previously discussed reasons, the combination of Circello and Fasang fail to obviate the present invention and the rejected claims should now be allowed.

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In view of the foregoing amendment and remarks, it is respectfully submitted that the application as now presented is in condition for allowance. Early and favorable reconsideration of the application are respectfully requested.

No additional fees are deemed to be required for the filing of this amendment, but if such are, the Examiner is hereby authorized to charge any insufficient fees or credit any overpayment associated with the above-identified application to Deposit Account No. 50-0320.

If any issues remain, or if the Examiner has any further suggestions, he/she is invited to call the undersigned at the telephone number provided below. The Examiner's consideration of this matter is gratefully acknowledged.

Respectfully submitted, FROMMER LAWRENCE & HAUG LLP

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